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CENTRAL FAX CENTERDocket No. 031948-3  
Application No. 10/693,903  
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**IN THE SPECIFICATION:**

Please replace the second full paragraph with the following paragraph:

--Some integrated circuits include a central processing unit (CPU) that needs to receive two clock signals having different frequencies and a predetermined phase relationship. For example, it may be necessary for all transitions of the lower-frequency clock signal to occur when the higher-frequency clock signal is in a specified (high or low) state. Although it is an easy matter to generate two clock signals satisfying this type of condition, the phase relationship between the clock ~~signal~~ signals may be altered by, for example, different propagation delays on the clock signal supply paths, so that by the time the clock signals reach the CPU, the necessary relationship may no longer obtain.--

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